

APRIL AI Hub Annual Summit 2026

Programme

Tuesday, 27 October

**The Edge, 34 Endcliffe Cres,
Endcliffe, Sheffield, S10 3ED**

09:00	10:00	Refreshments and Registration	Café Bar
10:00	10:15	Opening Remarks	High Tor 2
10:15	10:50	Three Years of APRIL	High Tor 2
10:50	11:10	Refreshments and Comfort Break	Café Bar
11:10	12:15	Seed Funding Talks Part 1	High Tor 2
12:15	13:00	Lunch Break & Poster Session	Dining Room
13:00	13:30	APRIL Tools: STARS	High Tor 2
13:30	14:30	Sandpit Competition - STARS Proposal Pitching Session	High Tor 2
14:30	14:50	Refreshments and Comfort Break	Café Bar
14:50	15:20	APRIL Tools: PRAMANA	High Tor 2
15:20	16:20	Sandpit Competition - PRAMANA Proposal Pitching Session	High Tor 2
16:20	16:30	Day 1 Closing Remarks	High Tor 2



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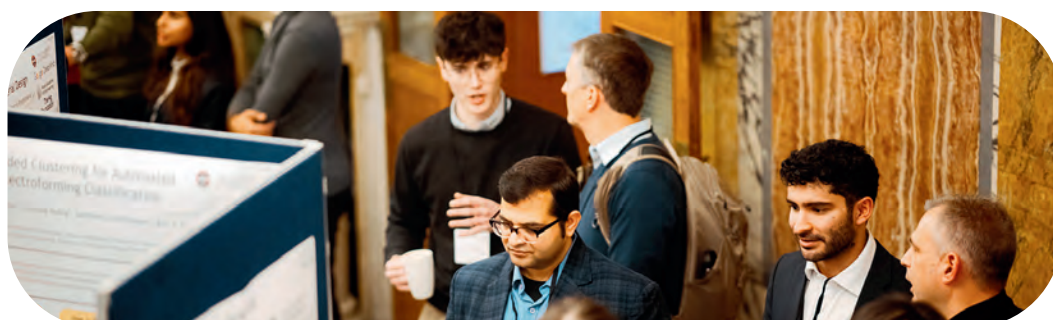
Wednesday, 28 October

**The Edge, 34 Endcliffe Cres,
Endcliffe, Sheffield, S10 3ED**

09:00	09:30	Refreshments and Registration	Café Bar
09:30	09:45	Opening Remarks	High Tor 2
09:45	10:15	APRIL Tools: EMOS	High Tor 2
10:15	11:15	Sandpit Competition - EMOS Proposal Pitching Session	High Tor 2
11:15	11:35	Refreshments and Comfort Break	Café Bar
11:35	12:05	APRIL Tools: LaMDA	High Tor 2
12:05	13:05	Sandpit Competition - LaMDA Proposal Pitching Session	High Tor 2
13:05	14:00	Lunch & Poster Session	Dining Room
14:00	15:00	PhD Presentations	High Tor 2
15:00	15:40	Seed Funding Talks Part 2	High Tor 2
15:40	16:00	Refreshments and Comfort Break	Café Bar
16:00	16:20	Awards Session	High Tor 2
16:20	16:30	Summit Closing Remarks	High Tor 2



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Tuesday, 27 October 11:10 - 12:15
Seed Funding Presentations

Dr. Chongyan Gu	Queen's University Belfast	AI-assisted hardware security model for semiconductor
Dr. Naveen Kumar	University of Glasgow	AI-Driven Inverse Design for Enhanced Photonics: Surface Roughness and NonLinearity Optimized Waveguides (AIR-SNOW)
Dr. Haijun Fan	Heriot-Watt University	AI-Enabled Power Amplifier Topology Automation
Dr. Naveen Kumar	University of Glasgow	AI-Enhanced Reliability Optimization in SRAM Cells under Process-Induced Variability
Cameron Barker	The University of Edinburgh	Efficient AI for Novel Hardware through Neural Architecture Search
Dmitry Nikolaenko	Durham University	EPITONIC: Enhancing Performance and Energy Efficiency of Tsetlin Machines through Non-Volatile Computing
Dr. Qiang Huang	Sunderland University	Improving Anomaly Detection in Microscopic Images of Integrated Circuits Using Deep Learning Technologies
Aquila Powell	Cardiff University	Robust Artificial Neural Network Device Modelling for High-Frequency Nonlinear Devices (RANN)
Dr. Jude Haris	University of Glasgow	SECDA-DSE: Automated Design Space Exploration of FPGA-based Accelerators using LLMs
Prof. Phil King	University of St Andrews	Towards self-driving thin-film synthesis of 2D Materials

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Wednesday, 28 October 14:50 - 15:30
Seed Funding Presentations

Ismaeil Alnaab	University of Southampton	An AI Tool for Accelerating the Throughput of 2D Material Transfer and Characterisation
Nicholas Smoliak	University of Glasgow	Modelling with AI for Resistive Switching (MARS)
Dr. Akhil Shaji	University of Bristol	Optimisation of complex semiconductor device design using machine learning in TCAD
Dr. Ben Mills	University of Southampton	Predicting Nanofabrication Outcomes with Deep-Learning
Prof. Mark Zwolinski	University of Southampton	AI-Guided Functional Verification of Systems on Chip
Dr. Han Wu	Newcastle University	EDABench: An Open-Source Benchmarking Framework for Machine Learning in Electronic Design Automation
Dr. Nema Abdelazim	University of Southampton	Learning and predicting the aging properties of Quantum Security Identifier



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Wednesday, 28 October 13:50 - 14:50
PhD Students' Presentations

**Matthew
Youngman**

A Standardised, Physically-Aware LLM Orchestrator for FPGA-Specific Multi-Agency

Shu Gu

Agent Control Interfaces for LLM-Guided FPGA Post-Implementation Optimisation

**Eva Hita
Sogorb**

Electronic Circuit Optimisation with Graph and Sheaf Neural Networks

Eleni Tselepi

Efficient Hardware Acceleration of Vision Generative AI Models on Edge Devices

**Semih
Vazgecen**

In-Silicon Edge Image Processing Using Memristive Architectures

Lai Gan

STARS: Data Collection Infrastructure and Device Performance Classification



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